PATENT APPLICATION **DOCKET NO.: 9903-078 CLIENT NO.: S02US013D**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Young-Hee SONG, et al.

Serial No.: 10/651,813

Examiner:

Willams, Alexander O.

Filing Date:

August 28, 2003

Group Art Unit:

2826

Title:

SEMICONDUCTOR CHIP HAVING BOND PADS AND MULTI-CHIP PACKAGE

INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Continued)

U.S. PATENT DOCUMENTS

Exam <u>Init</u>	Ref	Document Number	Issue <u>Date</u>	<u>Name</u>	Class	Sub <u>Class</u>
ev		4,723,197	Feb. 2, 88	Takiar et al.		
-84		5,502,289	Mar. 26, 96	Takiar et al		
-px		5,834,844	Nov. 10, 98	Akagawa et al		
-pr		6,175,149	Jan. 16, 01	Akram		
-p3		6,498,396 B1	Dec. 24, 02	Arimo		

FOREIGN PATENT DOCUMENTS

Exam <u>Init</u>	Ref	Document Number	Publication <u>Date</u>	Country	Name
æ		DE 19610302 A1	Oct. 2, 96	Germany, (Corresponds to US 6,498,396 B1)	
<u> 17</u>		EP 1094517 A2 .	Apr. 25, 01	Europe	
_17		EP 0221496 A2	May 13, 87	Europe	
-pr		2000058743 A	Feb. 25, 00	Japan	

OTHER DOCUMENTS

Exam <u>Init</u>

Ref

Author, Title, Date, Pertinent Pages, Etc.)

Yasunaga, et al. "Chip Scale Package: "A Light Dressed LSI Chip" IEEE transactions on component, packaging and Manufacturing technology part A.

Vol. 18, No. 3. September 1995, pp. 451-457

Examiner:

Date Considered: